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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/600,590	07/19/2000	BERNARD ASPAR	025219-268 5219		
7590 07/13/2006		EXAMINER			
ROBERT E. KREBS			KRUER, KEVIN R		
THELEN REID & PRIEST LLP P.O. BOX 640640			ART UNIT	PAPER NUMBER	
SAN JOSE, CA 95164-0640			1773		
			DATE MAILED: 07/13/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

<u> </u>			1.			
•		Application No.	Applicant(s)	=		
Office Action Summary		09/600,590	ASPAR ET AL.			
		Examiner	Art Unit	_		
		Kevin R. Kruer	1773			
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the c	correspondence address			
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period we are to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status						
1)⊠	Responsive to communication(s) filed on 17 Ag	oril 2006.				
	This action is FINAL . 2b) This action is non-final.					
3)[) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
5)□ 6)⊠ 7)□	Claim(s) 13-17,35 and 51-53 is/are pending in the same state of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) 13-17,35 and 51-53 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	vn from consideration.				
Applicati	on Papers					
10)⊠	The specification is objected to by the Examiner The drawing(s) filed on 19 July 2000 is/are: a) Applicant may not request that any objection to the Carelacement drawing sheet(s) including the correction of the oath or declaration is objected to by the Example 11 in the content of the carelacement of the	☑ accepted or b)☐ objected to b drawing(s) be held in abeyance. See on is required if the drawing(s) is obj	e 37 CFR 1.85(a). sected to. See 37 CFR 1.121(d).			
Priority ι	ınder 35 U.S.C. § 119					
12)⊠ a)l	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau See the attached detailed Office action for a list of	have been received. have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No ed in this National Stage			
Attachmen	t(s)					
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary				
3) 🔯 Inforr	e of Dransperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date 10/17/2005.	Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	atent Application (PTO-152)			

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DETAILED ACTION

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Claim Rejections - 35 USC § 112

- 1. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 2. The rejection of claims 1, 19-24, 32-34 and 50 under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement has been overcome by amendment. Said claims have been canceled.
- 3. Claims 52 and 53 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. There is no explicit support in the original disclosure for the intermediate layer that is "formed or transferred o the surface of said substrate, the bonding interface being located between said structure and said at least one intermediate layer formed or transferred on the surface of said carrier."

 Furthermore, there is no support in the original disclosure for the intermediate layer "formed or transferred on the thin layer."

Applicant argues said limitation is supported by the original drawings and the disclosure of page 19 of the specification. The examiner has fully reviewed the specification and figures and cannot find support for said limitation. Applicant is asked to specifically point where in the claims and figures said limitation is supported.

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Claim Rejections - 35 USC § 102

1. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

2. Claims 13-17, 35, and 51-53 are rejected under 35 U.S.C. 102(b) as being anticipated by Bisaro et al (US 5,141,894).

Bisaro teaches a compliant substrate as depicted in Figure 4d. The laminate comprises a substrate (10), a monocrystalline zone (13) made by implanting ions through the substrate (col 4, lines 64+), a preliminary layer (11) on said monocrystalline zone (col 4, line 52+), an epitaxial growth layer (16) on said preliminary layer (col 5, lines 12+) which can be ion implanted (15), and a final layer of epitaxial growth layer (17) comprising GaAs. Ion that can be implanted include Mn, Al, Si, Cr, Fe, Ni Co, Cu, Ge, Sn, Zn Cd, Ti C, Cl, B, Ar, P, Le, Au, Ni, oxygen, hydrogen, fluorine, Si, Br, and S (col 3, lines 37+). The layers may comprise crystalline, semiconductor materials such as silicon, germanium, or the like (col 6, lines 34+). As the laminate is used as a compliant substrate for epitaxial growth, said microcrystalline zone and/or microcavities are understood to absorb in whole or in part the stresses brought to said compliant substrate.

With regard to claim 35 and the claims that depend therefrom, the ion implantation of the substrate reads on the claimed "bonding interface" of claim 35. The ion implantation of the substrate is taught to create anchoring points that are centered at a depth Rp and having a width of 2.35XR0 (col 3. lines 46+). The epitaxial growth layer reads on the claimed "thin layer" of claim 35. The claimed "intermediate layer" of claim

15 is met by the preliminary layer (11). Bisaro teaches the intermediate layer may be made from GaAs (see Fig 4d), arsenic, gallium, Si, or a number of other materials (col 4, lines 57+). Since the layer is amorphous, the examiner takes the position it is inherently "non-homogeneous." With regards to claims 52 and 53 the examiner takes the position that the method in which said intermediate layer is formed does not patentably distinguish the claimed invention from the invention taught in Bisaro. The examiner notes said intermediate layer may be formed directly on the substrate (Fig 4a) and the bonding interface is located between the thin layer and the intermediate layer (see 4d).

With respect to claim 13, the bonding energy between the epitaxial growth layer (16) (which reads on the claimed "the thin layer") and the epitaxial growth layer (17) is altered by ion implantation. Ion implantation is known to affect the surface's roughness that would read on the claimed "defects."

Response to Arguments

Applicant's arguments filed April 17, 2006 have been fully considered but they are not persuasive.

Applicant argues disagrees with the examiner's assertion that the ion implantation of the substrate disclosed by Bisaro reads on the claimed "bonding interface." Applicant argues the term "bonding interface" is a commonly term in the semiconducting technology which refers to a surface which is common between two individual elements in contact. Anchoring points centered at a depth Rp inside a bulk

substrate cannot be assimilated to an interface. The examiner notes the definition noted by applicant in the "Semiconductor water bonding: science and technology" reference was not included with the action. Thus, the examiner is unable to give any fully weigh applicant's argument. The examiner further notes applicant's argument is not consistent with the disclosure of the specification. Specifically, the specification states (page 10, lines 12+) that ion implantation at the interface is within the scope of the disclosed invention. Thus, the examiner maintains the rejection for reasons of record.

The 35 U.S.C.112, first paragraph rejection and the 103 rejections of the non-final office action have been overcome by amendment. Therefore, applicant's arguments with regards to said rejections are moot.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

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the advisory action. In no event, however, will the statutory period for reply expire later

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than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Kevin R. Kruer whose telephone number is 571-272-

1510. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Carol Chaney can be reached on 571-272-1284. The fax phone number for

the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

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Business Center (EBC) at 866-217-9197 (toll-free).

Kevin R. Kruer

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Patent Examiner-Art Unit 1773